



"Gate Technology for Strained Surface Channel and
Strained Buried Channel MOSFET Devices"
Inventors: Fitzgerald et al.
Serial No.: 09/923,207 Filed: August 6, 2001
Attorney Docket No.: ASC-042
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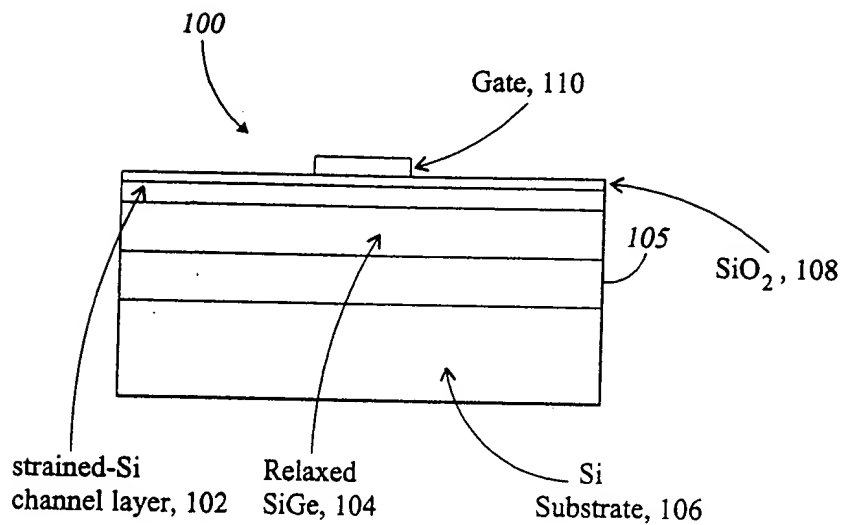


FIG. 1A

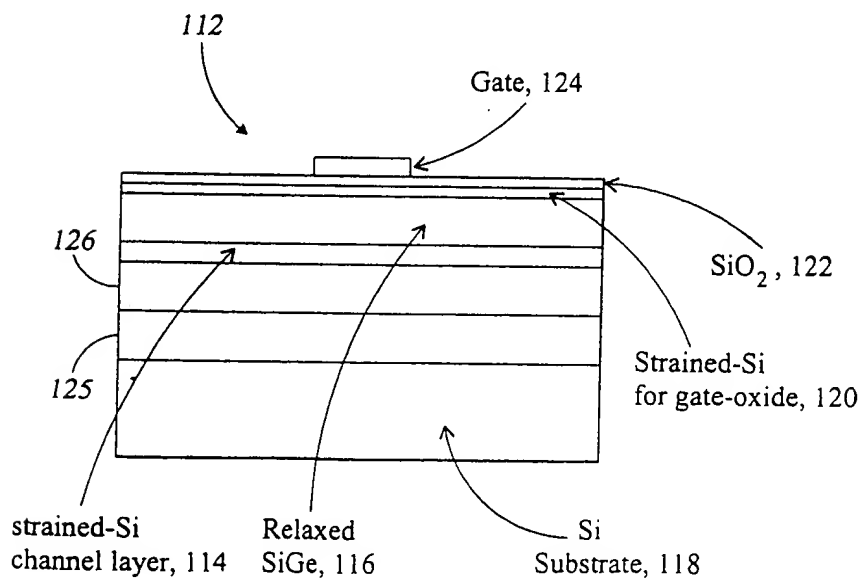


FIG. 1B



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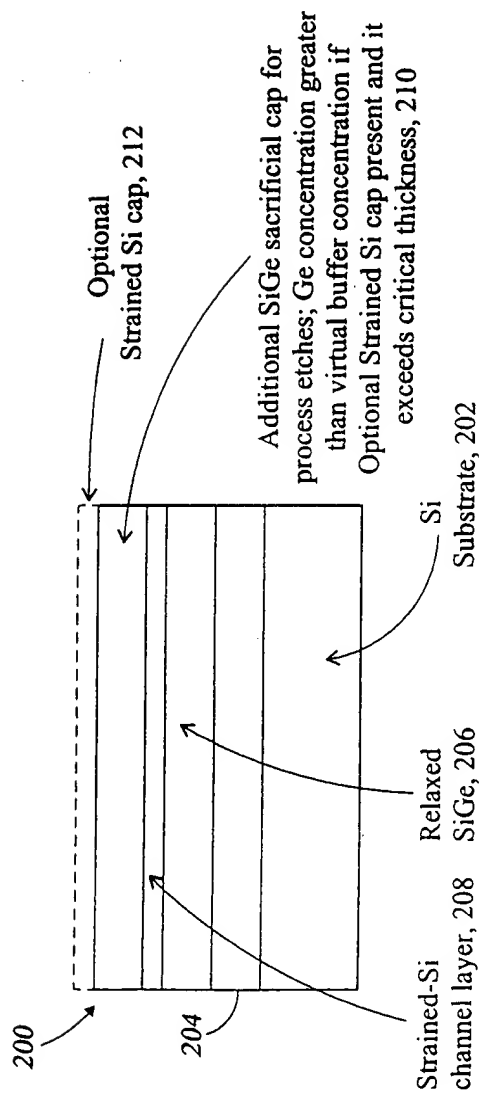


FIG. 2A



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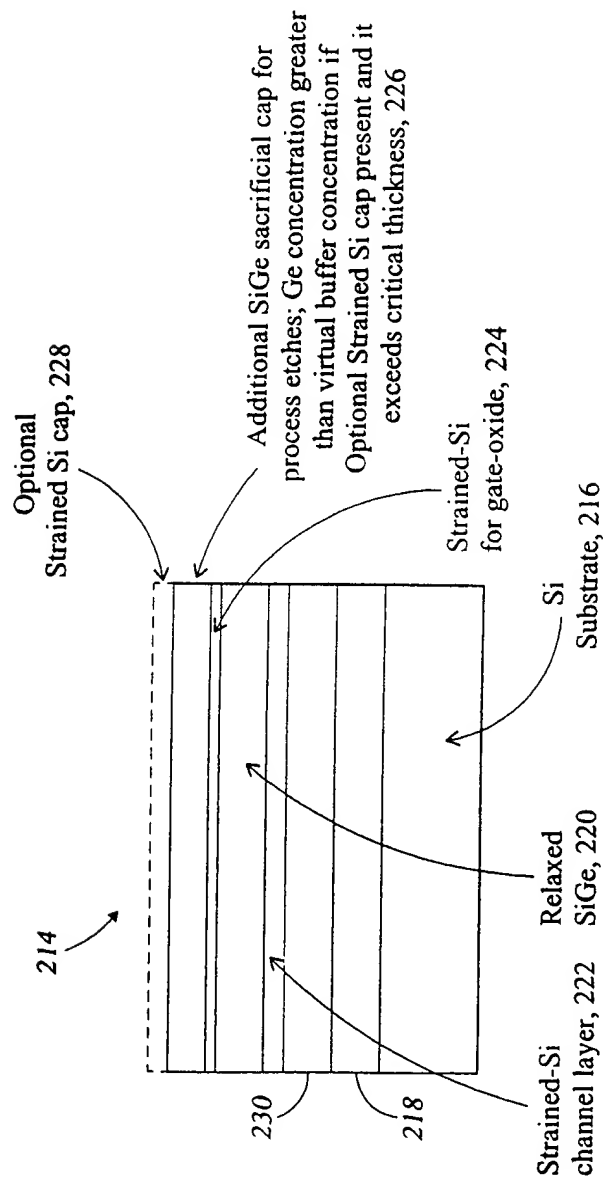


FIG. 2B



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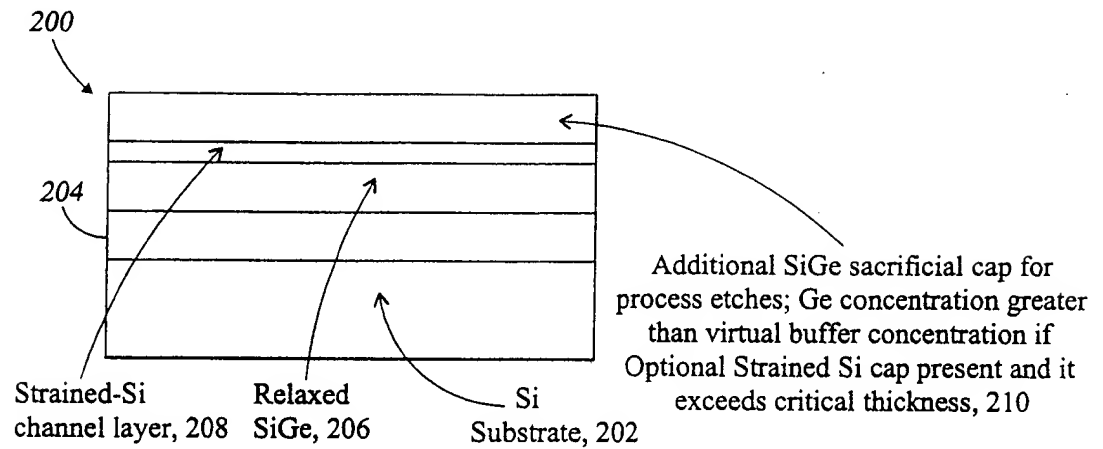


FIG. 3A

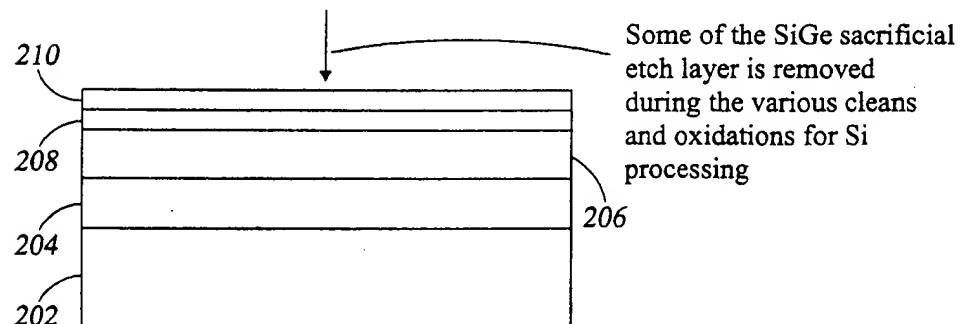


FIG. 3B



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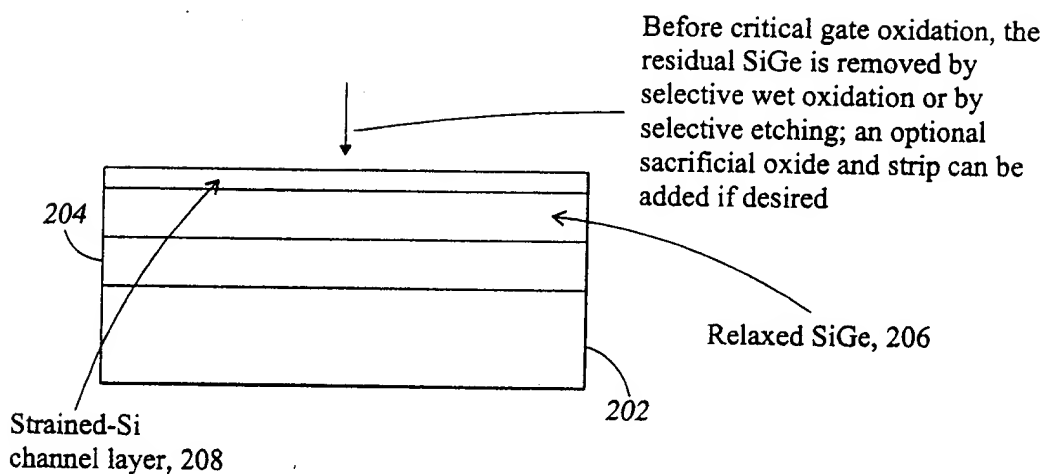


FIG. 3C

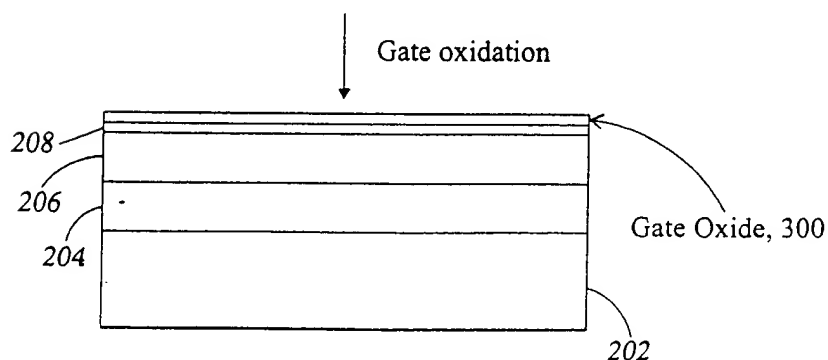


FIG. 3D



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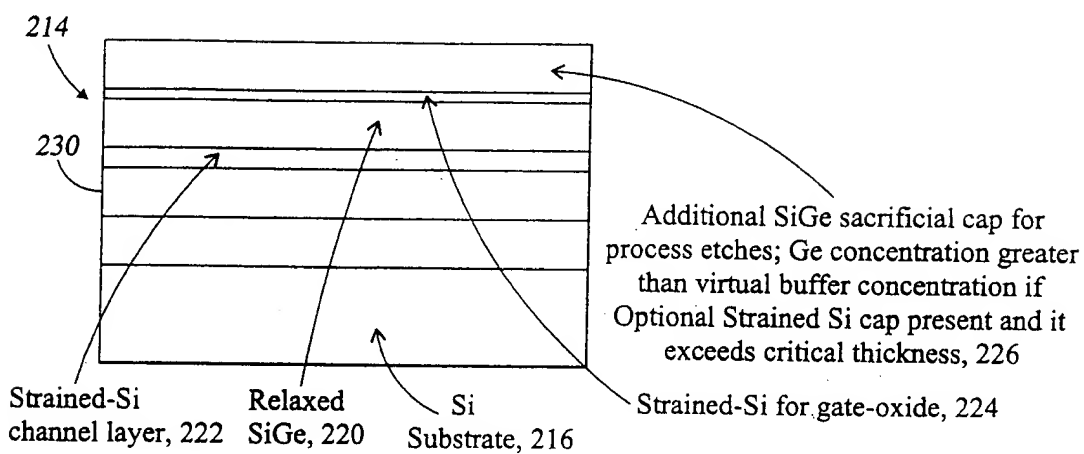


FIG. 4A

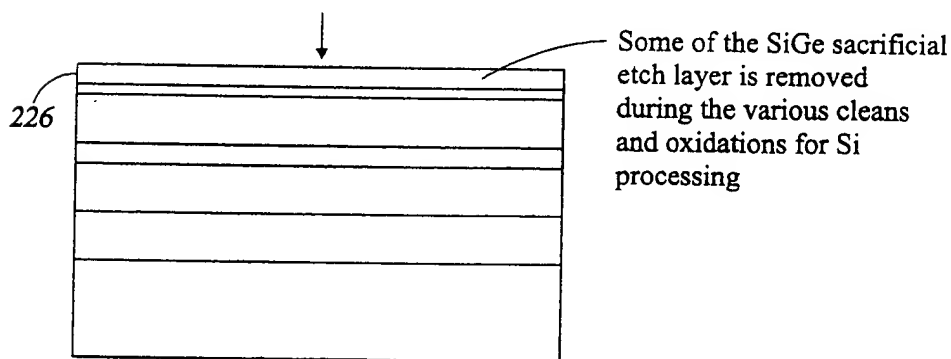


FIG. 4B



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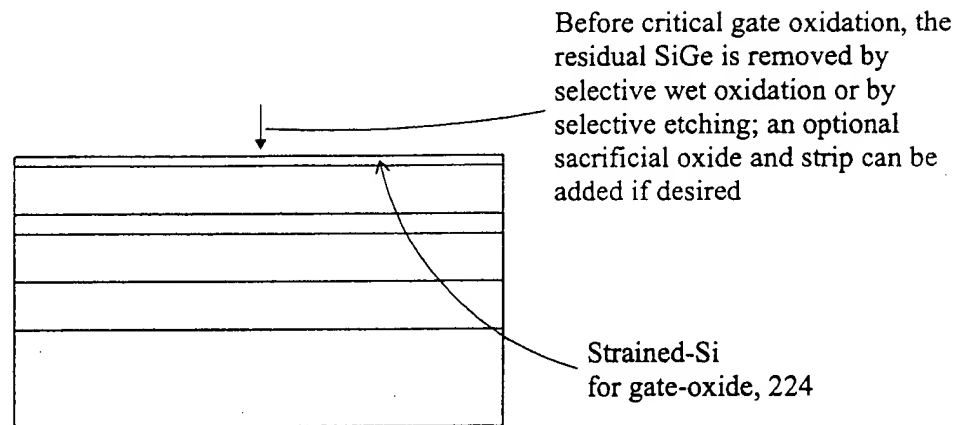


FIG. 4C

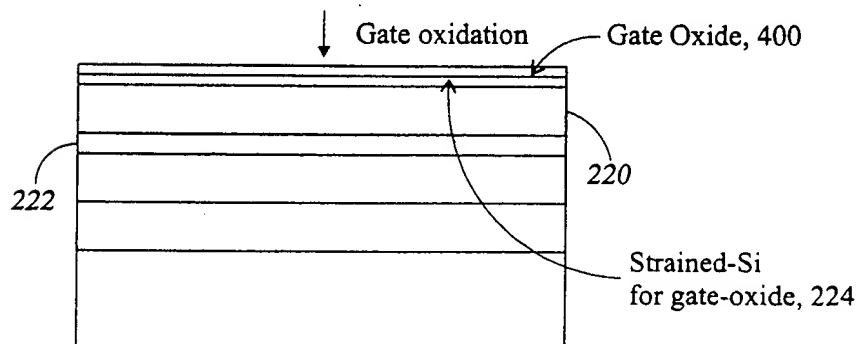


FIG. 4D



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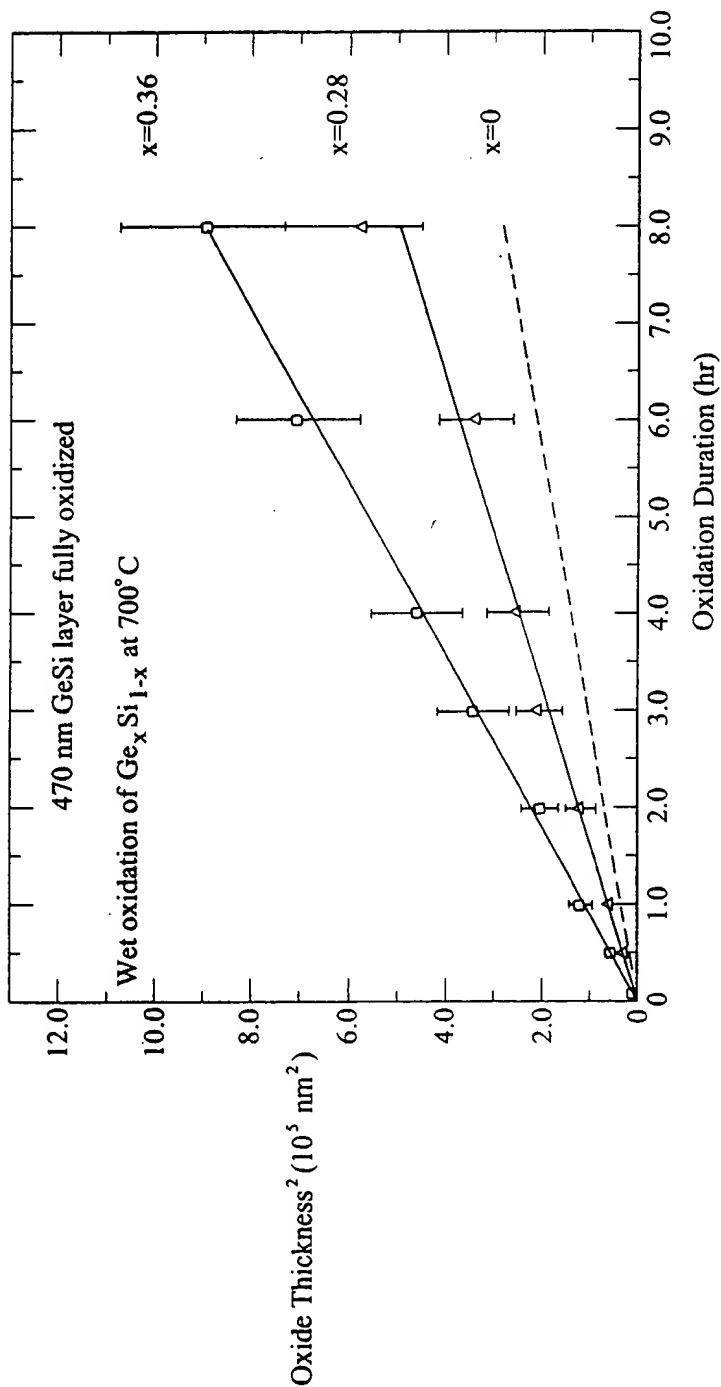


FIG. 5



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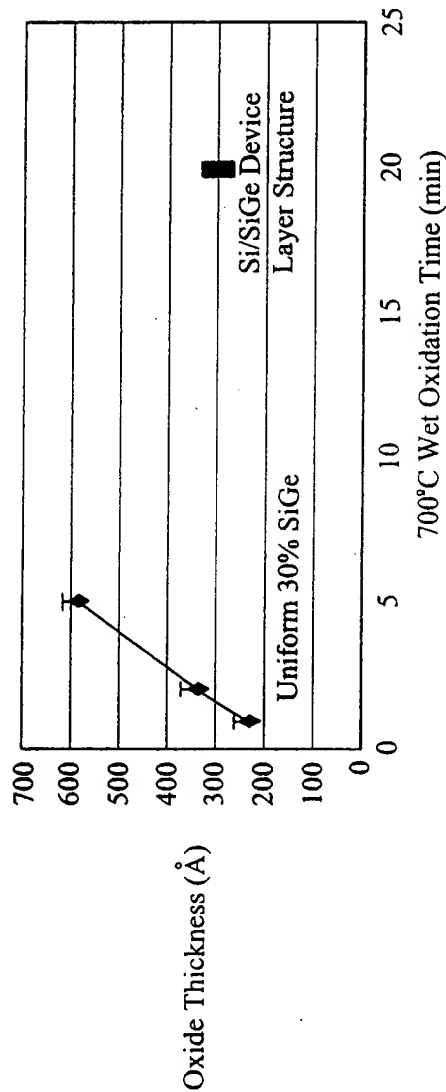


FIG. 6

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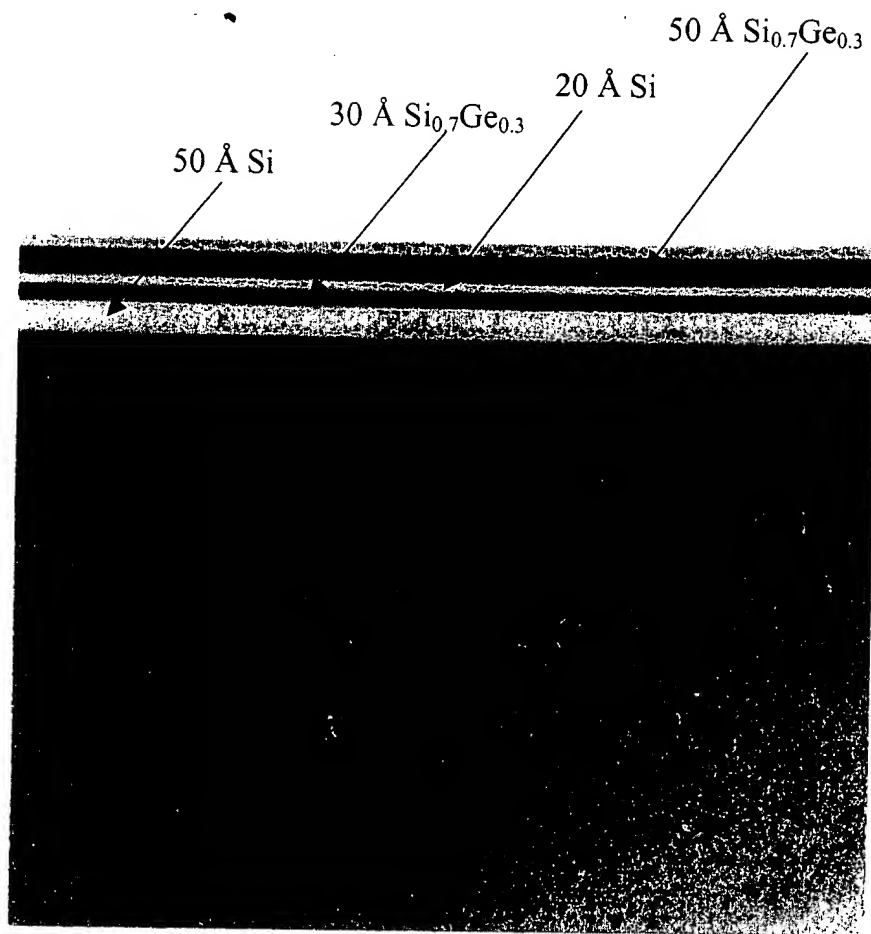


FIG. 7

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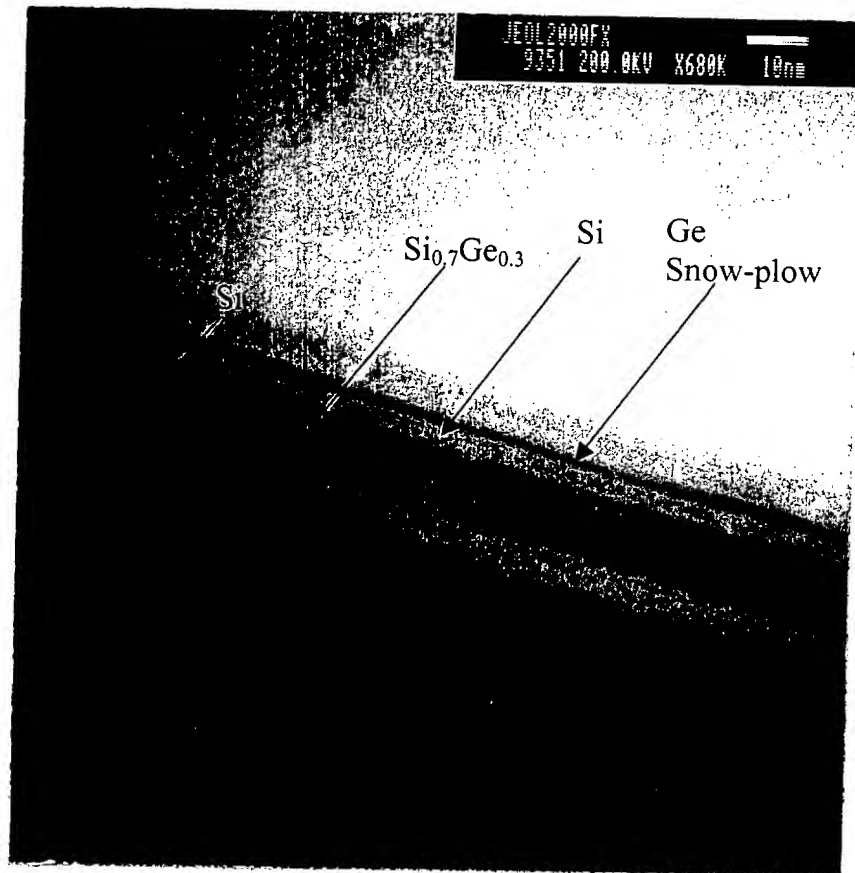


FIG. 8

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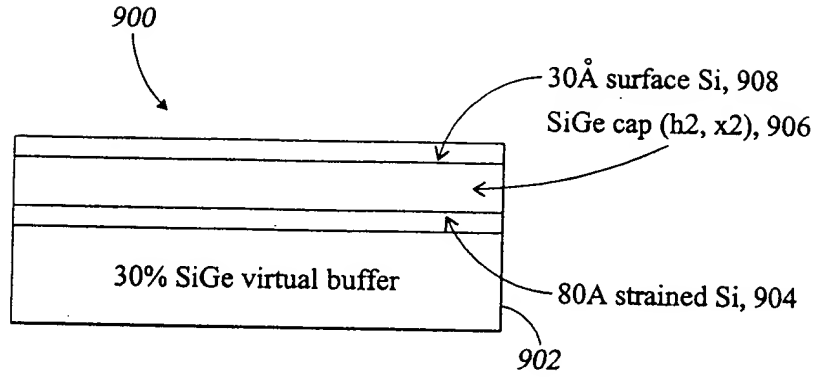


FIG. 9

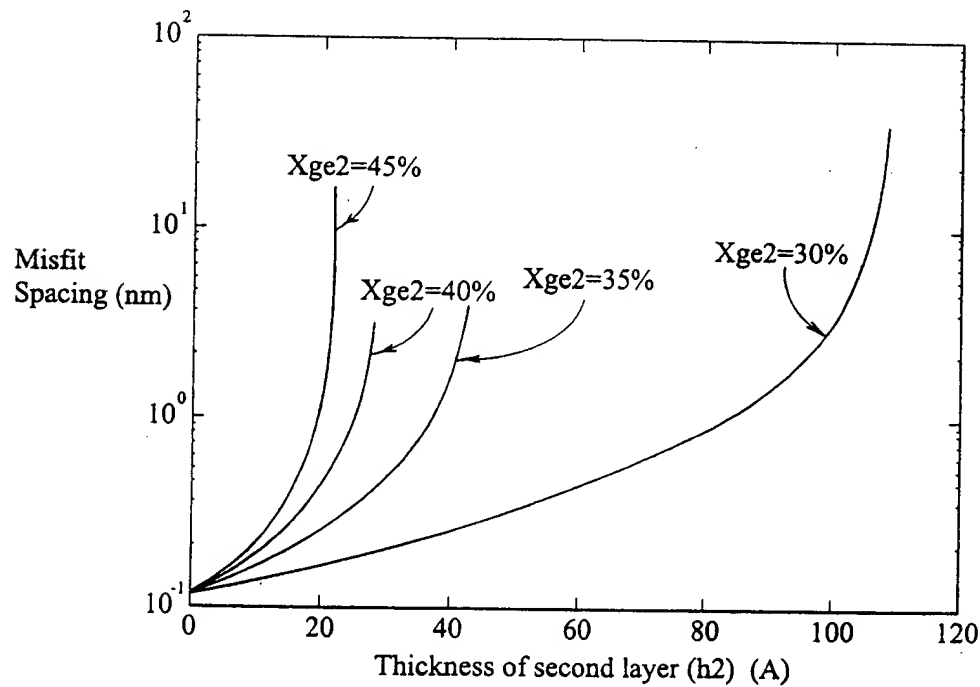


FIG. 10